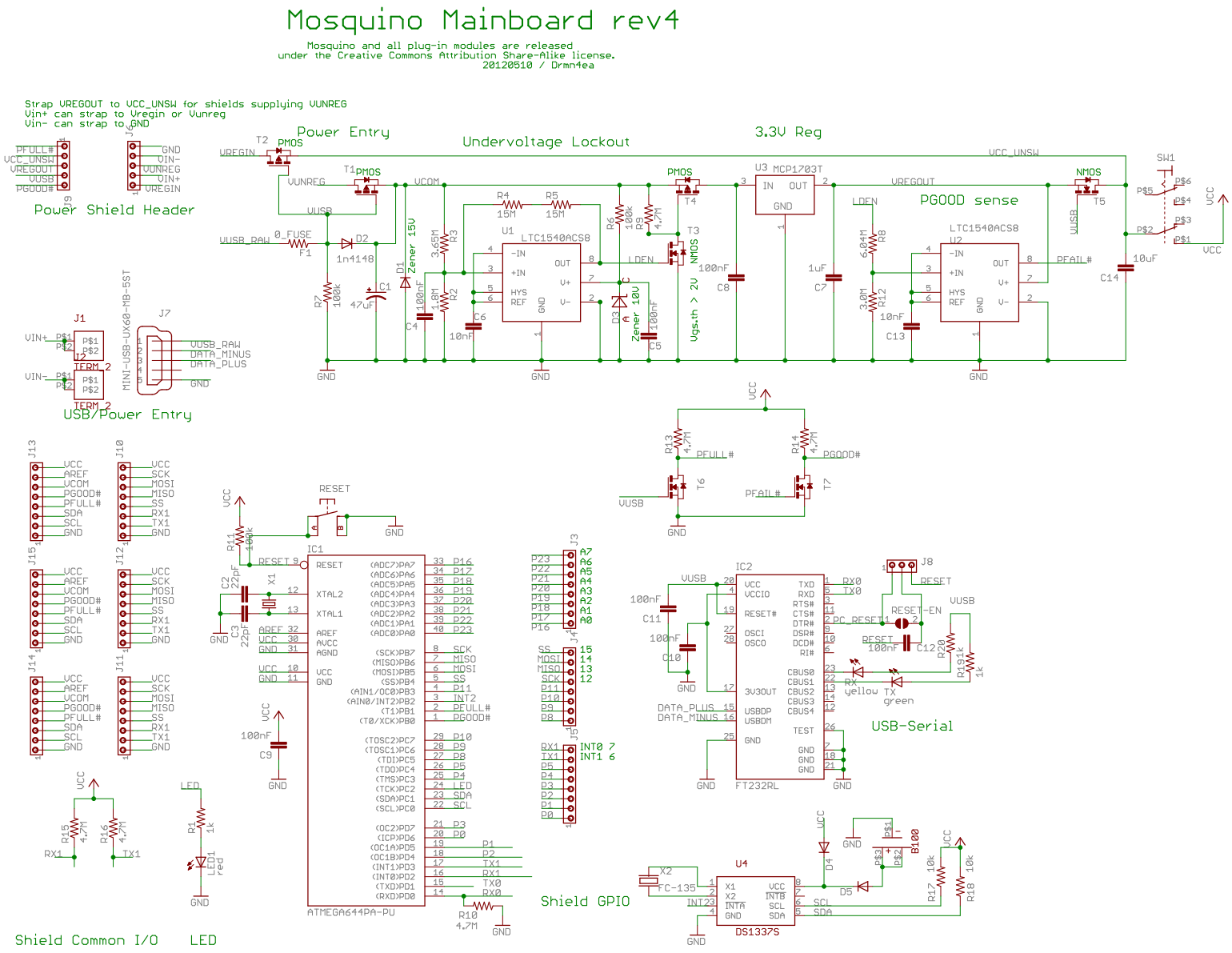
Mosquino Mainboard rev4
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20120510 / Drmn4ea
Strap VREGOUT to VCC_UNSW for shields supplying VUNREG
Vin+ can strap to Vregin or Vunreg
Vin- can strap to GND
Power Shield Header
Power Entry
Undervoltage Lockout
3.3V Reg
PGOOD sense
USB/Power Entry
Shield Common I/O
LED
Shield GPIO
USB-Serial
Realtime Clock
ATMEGA644PA-PU
FT232RL
DS1337S
LTC1540ACS8
MCP1703T